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Inventor Name Search Result

D.

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Last Name = DURCAN

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Serial#	Patent#	Status	Date Filed	Title	Inventor Name
07526079	Not Issued	161	05/10/1990	METHOD FOR REDUCING CROSS-SECTIONAL ASYMMETRY IN SPACER STRIPS USED	DURCAN , MARK
08862752	Not Issued	94	05/23/1997	PLANARIZATION PROCESS FOR SEMICONDUCTOR SUBSTRATES	DURCAN , MARK
09146116	Not Issued	93	09/02/1998	SEMICONDUCTOR PROCESSING METHOD USING HIGH PRESSURE LIQUID MEDIA	DURCAN , MARK
09311914	6100162	150	05/14/1999	METHOD OF FORMING A CIRCUITRY ISOLATION REGION WITHIN A	DURCAN , MARK
09334261	Not Issued	71	06/16/1999	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	DURCAN , MARK
09518558	Not Issued	30	03/03/2000	METHOD OF FORMING A CIRCUITRY ISOLATION REGION WITHIN A SEMICONDUCTIVE	DURCAN , MARK
09603849	Not Issued	71	06/26/2000	SEMICONDUCTOR PROCESSING METHOD USING HIGH PRESSURE LIQUID MEDIA TREAT	DURCAN , MARK
09645582	Not Issued	30	08/25/2000	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	DURCAN , MARK
09503638	Not Issued	30	02/14/2000	RANDOM ACCESS MEMORY	DURCAN , MARK D.

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